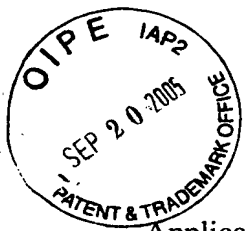


09-22-05

2833



Express Mail No.: EV 533 715 600 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Donald V. Perino, et al.

Confirmation No.: 4507

Serial No.: 09/961,114

Art Unit: 2833

Filed: September 20, 2001

Examiner: Felix Figueroa

For: *Chip Socket Assembly and Chip File Assembly for Semiconductor Chips*

Attorney Docket No: 060809-5054-US

PETITION FOR EXTENSION OF TIME UNDER 37 CFR § 1.136(a)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the time for response to the Final Office Action dated March 21, 2005 be extended to a period of three months from June 21, 2005 to and including September 21, 2005.

Applicants are hereby filing this extension of time to establish continuity between this application and a continuing application filed under 37 CFR § 1.53(b) on September 20, 2005 entitled "*Chip Socket Assembly and Chip File Assembly for Semiconductor Chips.*"

The fee for this extension is estimated to be \$ 1,020.00. Please charge the required fee or any additional fee to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310 (order no. 060809-5054-US). A copy of this sheet is enclosed.

Date: September 20, 2005

Respectfully submitted,

Dion M. Bregman

45,645

(Reg. No.)

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